

**Mr. Rajender Kumar**

**Designation:** Assistant Professor



**Brief Bio-data:**

Rajender Kumar received his Bachelor of Engineering (B.E) degree in Electronics and Communication Engineering from Maharishi Dayanand University, Rohtak, Haryana, in 2001 and his Master of Technology (M.Tech) in VLSI Design and Embedded System from Guru Jambheshwar University of Science and Technology, Hisar, Haryana, in 2008. He has teaching experience of about 15 years. Presently, he is working as Assistant Professor at BPS Mahila Viswavidyalay, Sonipat, Haryana since 14<sup>th</sup> July, 2009. He is pursuing Ph.D from SMVD University, Katra (Jammu & Kashmir). His research interests are in the area image and video processing and VLSI design for video processing and its implementation in VLSI. He has published more than 5 papers in SCI/SCIE journals, conference proceedings and including 5 expert talks. He has also conducted many workshops and training in VHDL, Image Processing and Open Source Softwares such Scilab etc.

**E-Mail:** [rajender78@bpswomenuniversity.ac.in](mailto:rajender78@bpswomenuniversity.ac.in)

**Contact No.:** 9416779829